

## CTTV3624F Series From 100nH to 220nH



### CHARACTERISTICS

**Description:** SMD Trans-inductor Voltage Regulator Inductors  
**Applications:** Data centers, Graphic Cards, CPU  
**Operating Temperature:** -40°C to +125°C (including self-temperature rise)  
**Storage Temperature:** -40°C to +125°C (on board)  
**Inductance Test Frequency:** 100kHz/1.0V  
**Irms(A):** Heat rated current will cause the coil temperature rise approx.  $\Delta T$  of 40°C.  
**Isat 1(A):** Saturation current will cause inductance L0 to drop approx. 20% at +25°C.  
**Isat 2(A):** Saturation current will cause inductance L0 to drop approx. 20% at +100°C.  
**Isat 3(A):** Saturation current will cause inductance L0 to drop approx. 20% at +125°C.  
**Rated Current:** The lower value of Isat and Irms.  
**Storage Condition (Component in its packaging)**

- Temperature: Less than 40°C
- Humidity: Less than 60% RH

**Miscellaneous:**

- All test data referenced to 25°C ambient
- RoHS Compliant

**Samples available. See website for ordering information.**

### SPECIFICATIONS

Part Number	Inductance (nH) @0A 1-4/2-3 ±15%	Inductance (nH) @Isat1 Min.	DCR (mΩ) ±10%		Irms (A)		Isat 1 (A)	Isat 2 (A)	Isat 3 (A)	Marking
			1-4	2-3	1-4	2-3				
CTTV3624F-R10L	100	64	0.125	0.330	75	40	98	83	78	R10
CTTV3624F-R12L	120	77	0.125	0.330	75	40	79	67	63	R12
CTTV3624F-R15L	150	96	0.125	0.330	75	40	62	53	49	R15
CTTV3624F-R18L	180	115	0.125	0.330	75	40	54	46	43	R18
CTTV3624F-R22L	220	140	0.125	0.330	75	40	50	-	-	R22

### PHYSICAL DIMENSIONS

Top view

Bottom view

Recommended PCB Layout

Front view

Side view

**Notes:**

1. Flatness of PAD surface: 0.1mm Max.
2. Marking: Inductance (Please refer to Electrical Specifications table)

Size	A	B	C	D1	D2	D3	E	F
mm	9.30 ± 0.30	6.10 ± 0.30	10.20 ± 0.3	3.20 ± 0.30	1.15 ± 0.30	0.60 ± 0.30	2.20 ± 0.30	7.85 ± 0.30
inches	0.366 ± 0.012	0.240 ± 0.012	0.402 ± 0.012	0.126 ± 0.012	0.045 ± 0.012	0.024 ± 0.012	0.087 ± 0.012	0.309 ± 0.012