CTMMP73VCZF Series

From .10 μ H to 33 μ H



CHARACTERISTICS

Description: SMD (shielded) power inductor **Applications:** High density DC/DC converters. POL

converters. High current VRM/VRD for notebooks, servers, and

desktop CPUs. High speed chargers.

Operating Temperature: -55°C to +125°C [The part temperature (ambient + temp. rise)] should not exceed 125°C under worst case operating conditions. Component placement, PWB trace, size, thickness and other cooling provisions will affect the part temperature. Part temperature should be verified in the end application.

Inductance Tolerance: ±20%

Testing: Inducance is tested on an HP4285A at 100kHz, 0.5V

Packaging: Tape & Reel.

Marking: Parts are marked with inductance code.

Miscellaneous: RoHS Compliant.

Additional Information: Additional electrical & physical

information available upon request.

Samples available. See website for ordering information.

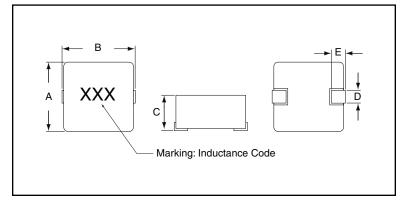
SPECIFICATIONS

Parts are only available in ±20% inductance tolerance only *Irms DC current (A) will cause an approximately ΔT of 40°C **Isat DC current (A) will cause L0 to drop approximately 30%

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Part Number	Inductance (µH)	L Test Freq. (KHz)	*Irms Typ. (A)	**Isat Typ. (A)	DCR Max. (mΩ)	DCR Typ. (mΩ)				
CTMMP73VCZF-R10M CTMMP73VCZF-R22M CTMMP73VCZF-R33M CTMMP73VCZF-R47M CTMMP73VCZF-R68M CTMMP73VCZF-R82M	0.10 0.22 0.33 0.47 0.68 0.82	100 100 100 100 100 100	37 23 20 16.5 15	45 40 33 27 24 23	1.5 2.8 4.2 5.5 6.3 8.0	1.3 2.5 4.0 5.0 5.7 7.5				
CTMMP73VCZF-1R0M CTMMP73VCZF-1R5M CTMMP73VCZF-2R2M CTMMP73VCZF-3R3M CTMMP73VCZF-4R7M CTMMP73VCZF-6R8M CTMMP73VCZF-6R8M	1.0 1.5 2.2 3.3 4.7 6.8 8.2	100 100 100 100 100 100 100	12 9.5 8.5 6.0 5.5 4.5	22 18 14 12 9 8 6	10 15 20 35 40 60	9.0 13.5 17.0 32.0 35.0 54.0 54.0				
CTMMP73VCZF-100M CTMMP73VCZF-150M CTMMP73VCZF-220M CTMMP73VCZF-330M	10 15 22 33	100 100 100 100	4.0 3.0 3.0 2.0	5.5 5.0 3.2 3.0	68 122 145 270	62.0 111 130 245				

PHYSICAL DIMENSIONS

Size	Α	B Max.	C Max.	D Typ.	E
mm	6.6±0.2	7.3	3.0	2.9	1.6±0.5
inches	0.26±0.008	0.29	0.12	0.11	0.063±0.02



PAD LAYOUT

